Amendments To The Specification

Please replace the title of the application in the specification page 1, line 1, with the following title:

XML PROCESSOR AND XML PROCESSING METHOD IN SYSTEM FOR XML PARSING HAVING THE XML PROCESSOR FORMED OF INDEPENDENT HARDWARE TO REDUCE PROCESSING TIME FROM THE XML PARSING

Please replace the 6th paragraph beginning on line 25 of page 4 in the Specification with the following amended paragraph:

As shown in FIG. 2, the XML processor ~~13~~ **17** according to the embodiment of the present invention and software-based conventional XML parsers 15 for special use process the same input XML document[s] 14 and generate the same product 16.

Please replace the following paragraphs beginning with the second paragraph on line 8 of page 8 and ending on line 4 of page 9 in the Specification with the following amended paragraphs:

FIG. 9 is a layer diagram illustrating a software architecture of a system that utilizes an XML processor according to an embodiment of the present invention. A variety of methods are used for an interface 56 between the system 51 **and** ~~**that utilizes**~~ the XML processor 13 according to one embodiment of the present invention ~~**and the XML processor 13**~~. For example, standard methods such as PCI (Peripheral Component Interface), USB (Universal Serial Bus), etc., are used. In consideration of speed and stability of the interface. As the system 51, a variety of embedded systems, such as cellular phones, digital home electronics, telematics terminals, PDAs, web TVs, and the like are used.

A device driver 55 of the system 51 is a program with the lowest level for transmitting and receiving data with the XML processor 13. An XML parser API 54 (Application Programming Interface) is upper the device driver 55. An application program 52 performs XML processing using the XML parser API 54. A language application layer 53 is intended to maintain independence of language when an application program is developed. The language application layer 53 has to be supported by its own language.

The XML parser API 54 is stored in a memory (not shown) of the system 51, and can be realized using a software program, which is executed by a processor (not shown). The XML parser API 54 supports XML grammar suitable for processing an XML document **50**. The XML processor 13 performs ~~**an XML document, as shown in FIG. 4, using**~~ a part of the XML processing~~**, e.g.,**~~ **using** the hardware processing module 42 that embeds the memory management function and the memory 43, thereby improving an XML processing speed**, a shown in FIG. 4**. The memory management function of the hardware processing module 42 in the XML processor 13 is shown and described with reference to FIGS. 5 through 8.

FIG. 10 is a layer diagram illustrating a software architecture of a system for large amounts of XML processing. System 60 shown in FIG. 10 has a similar configuration to that of the system ~~**50**~~ **51** shown in FIG. ~~**5**~~ **9**, and further includes a software XML parser 62.

Please replace the 7th paragraph beginning on line 28 of page 4 in the

Specification with the following amended paragraph:

FIG. 3 is a schematic diagram illustrating a capacity of an XML processor according to an embodiment of the present invention. Referring to FIG. 3, it is easily understood that the processing capacity of the XML processor according to the embodiment of the present invention is variably adaptable according to the request to the each of systems **30, 31, 32, and 33**.